

Class	Subject
ISSUE CLASSIFICATION	

PATENT NUMBER

U.S. UTILITY Patent Application

I.P.E. SCANNED <i>Am³</i> Q.A. <i>DT</i>	PATENT DATE
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TITLE	APPLICANTS	APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
		09/732240		257	777	2815 2815	<i>Chen</i>
		Thomas Ference Wayne Howell					
		Multi-chip stack and method of fabrication utilizing self-aligning electrical contact array					
		PTO-2040 12/89					

[illegible]

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS		CLAIMS ALLOWED	
	Sheets Drawn	Figs. Drawn	Print Fig.	Total Claims
<input checked="" type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner)		NOTICE OF ALLOWANCE MAILED	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent No. _____	_____ (Date)		ISSUE FEE	
	_____ (Primary Examiner)		Amount Due	Date Paid
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner)		ISSUE BATCH NUMBER	
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